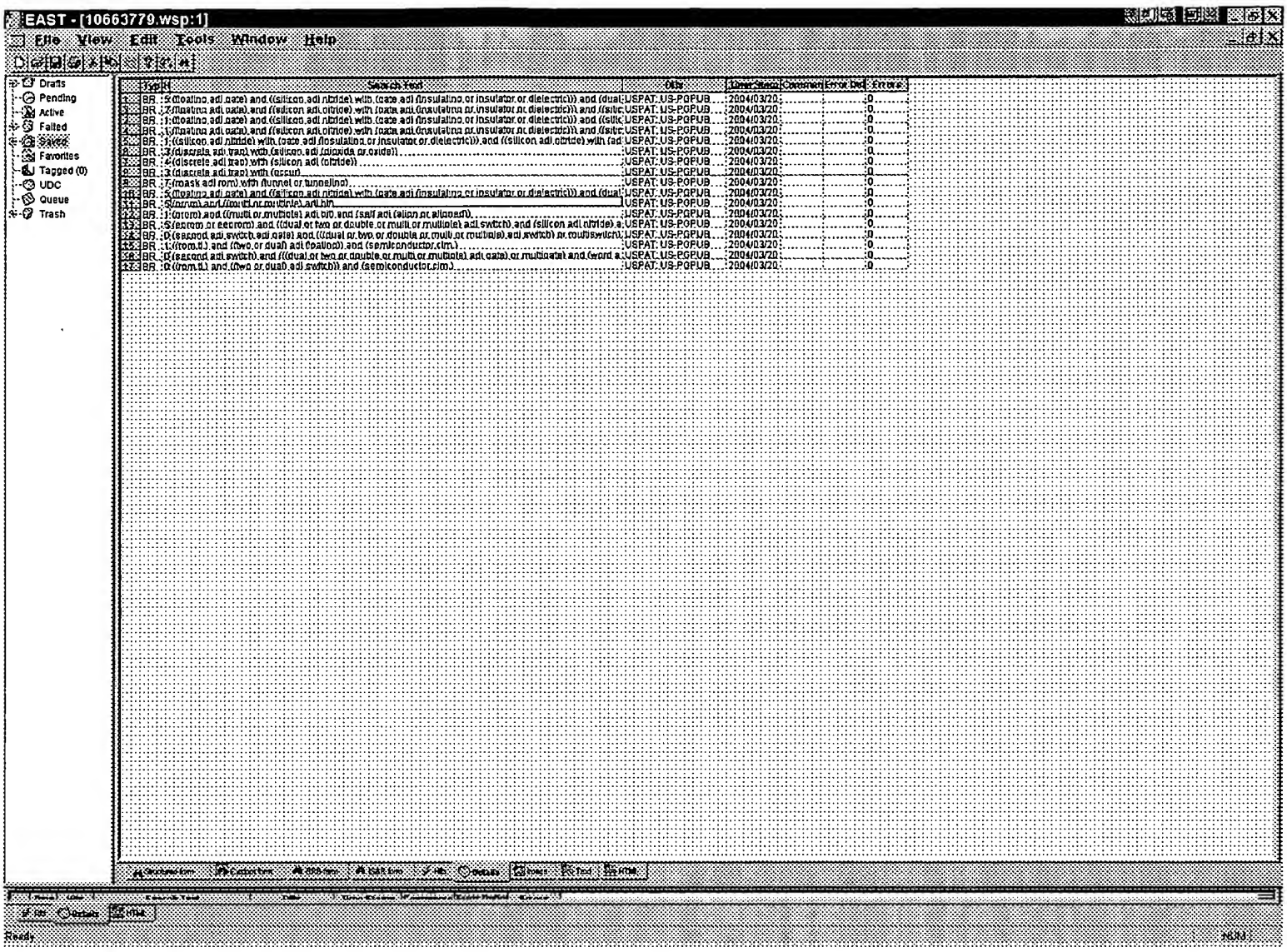
EAST - [10663779.wsp:1]
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Error
1 BR (5 (floating adj gate) and ((silicon adj nitride) with (gate adj (insulating or insulator or dielectric))) and (dual
2 BR (7 (floating adj gate) and ((silicon adj nitride) with (gate adj (insulating or insulator or dielectric))) and ((silic
3 BR (1 (floating adj gate) and ((silicon adj nitride) with (gate adj (insulating or insulator or dielectric))) and ((silic
4 BR (1 (floating adj gate) and ((silicon adj nitride) with (gate adj (insulating or insulator or dielectric))) and ((silic
5 BR (1 ((silicon adj nitride) with (gate adj (insulating or insulator or dielectric))) and ((silicon adj nitride) with (ad
6 BR (3 (discrete adj trap) with (silicon adj (dioxide or oxide))
7 BR (4 (discrete adj trap) with (silicon adj (nitride))
8 BR (3 (discrete adj trap) with (oxide))
9 BR (7 (mask adj rom) with (tunnel or tunneling))
10 BR (5 (floating adj gate) and ((silicon adj nitride) with (gate adj (insulating or insulator or dielectric))) and (dual
11 BR (5 (rom) and (((multi or multiple) adj bit) and (self adj (align or aligned))
12 BR (1 (prom) and (((multi or multiple) adj bit) and (self adj (align or aligned))
13 BR (5 (eprom or eeprom) and (((dual or two or double or multi or multiple) adj switch) and (silicon adj nitride) a
14 BR (0 (second adj switch adj gate) and (((dual or two or double or multi or multiple) adj switch) or (multiswitch)
15 BR (1 ((rom.ti.) and ((two or dual) adj floating)) and (semiconductor.clm.)
16 BR (0 ((second adj switch) and (((dual or two or double or multi or multiple) adj gate) or multigate) and (word a
17 BR (0 ((rom.ti.) and ((two or dual) adj switch)) and (semiconductor.clm.)
Ready
NUM